



TSBA2T100S(A)S-255A

10A/100V⁽¹⁾, low VF Schottky barrier diode with trench MOS structure

Mechanical Data

Chip Drawing	Item	Information	
	Die Size (A)	2590 μm	102 mil
	Top Metal Pad Size (B)	2481 μm	97mil
	Chip Size (C)	2489μm	98mil
	Wafer Thickness (D)	255μm	11.4 mil
	Scribe Line Width (E)	80 μm	3.15 mil
	Wafer Size	6 inch	
	Top Side Metallization	TSBA2T100SS-255A	Al/Ag
	Back Side Metallization	Ti Ni Ag	
	Recommended Storage Environment	Stored in original container, in dry nitrogen, (6 months at an ambient temperature of 23°C±3°C)	

Electrical Characteristics (T_J=25°C, unless otherwise specified)⁽²⁾

Parameter	Description	Min.	Typ.	Max.	Unit	Test Condition
V _{BR}	Reverse Breakdown Voltage	105	109	-	V	I _R =100μA
V _F	Instantaneous Forward Voltage	-	0.56	0.60	V	I _F =10A ⁽³⁾
I _R	Reverse Leakage Current	-	25	50	μA	V _R =105V
T _J , T _{STG}	Operating and Storage Temperature	-40°C to 150°C Max				

Note:

(1) The preliminary wafer datasheet only for reference;

(2) This characteristics assumes the dies are assembled in TO-220 packages. Actual performance may degrade when assembled.

YJ does not guarantee device performance after assembly;

(3) Pulse Width tp = < 300μs, Duty Cycle <2%;